Figure 1 - Predistortion PA circuit

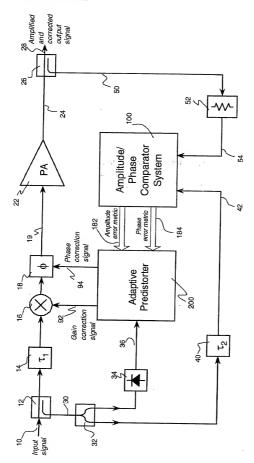


Figure 2 - Amplitude/Phase Comparator circuit (first embodiment)

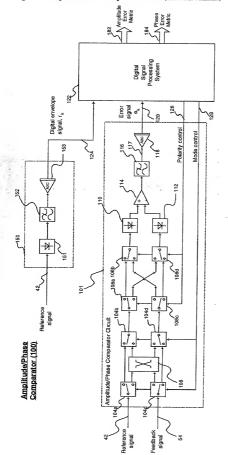


Figure 3 - Amplitude/Phase Comparator circuit (second embodiment) Digital Signaf Processing System Digital envelope signal, r<sub>k</sub> Error Polarity control Mode control 153 D C Ottod Confrol Circuit Amplitude/Phase Comparator Circuit Amplitude/Phase Comparator (100) Feedback signal

Figure 4 - Predistortion circuit for use with the present invention

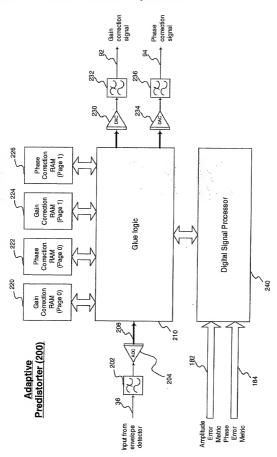


Figure 5 - Magnitude error computation

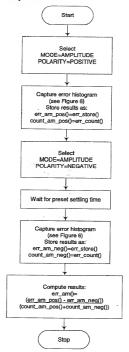


Figure 6 - Histogram capture computation

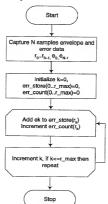
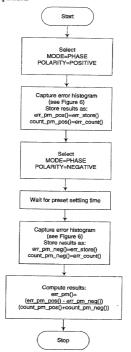


Figure 7 - Phase error computation



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